Application No.: 09/752,122

Docket No.: JCLA6705

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| In Re Application of: | | | | | | | JUN 2 1 2004 |
|-----------------------|---|---|--------------------|------------|---------|-----------|--------------|
| | | CHI et al. |)) | Examiner | : Barry | J. O'Brie | en |
| Serial No. | : | 09/752,122 |)) | Art Unit | : 2183 | | OEEICIAI |
| Filed | : | 12/29/2000 |)) | Docket No. | : JCLA6 | 5705 | OHIGINE |
| For | : | Memory Data Access Structure And Method Suitable For Use In A Processor | <i>)</i>)) | | | | |

No fee is believed to be due. However, the Commissioner is authorized to charge any fees required in connection with the filing of this paper to account No. 50-0710 (Order No. JCLA6705).

AMENDMENT AND RESPONSE TO OFFICE ACTION

Mail Stop Non-Fee Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The Office Action mailed April 22, 2004 has been carefully considered. In response thereto, please enter the following amendments and consider the following remarks.